

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Eric C. Saxe, et al.

Serial No.: 10/661,413

Filed on: September 12, 2003

) Confirmation No.: NYA
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) Examiner: NYA
)
) Group Art Unit No.: NYA
)

For: PROCESSOR SPECIFIC DISPATCHING IN A HETEROGENEOUS
CONFIGURATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

 X 37 C.F.R. §1.97(b).

 37 C.F.R. §1.97(c). If so, then this Information Disclosure Statement includes one of the following:

 A statement pursuant to 37 C.F.R. §1.97(e)

 1.97(e)(1) The undersigned hereby states that each item of information contained in this information disclosure statement was first cited in communication from a foreign patent office in a counterpart

foreign application not more than three months prior to the filing of this information disclosure statement.

_____ 1.97(e)(2) The undersigned hereby states that no item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in this information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of this information disclosure statement.

_____ A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

_____ 37 C.F.R. §1.97(d). If so, then this Information Disclosure Statement includes the following:

_____ A statement pursuant to 37 C.F.R. §1.97(e)

_____ 1.97(e)(1) The undersigned hereby states that each item of information contained in this information disclosure statement was first cited in communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement; OR

_____ 1.97(e)(2) The undersigned hereby states that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of this information disclosure statement.

AND

_____ A check for \$180.00 for the fee under 37 C.F.R. §1.17(i) for submission of the Information Disclosure Statement.

_____ 37 C.F.R. §1.97(i). Wherein applicants are submitting references before the grant of a patent to be placed in the file but not considered by the Patent office.


- (1) Accordingly, copies of the references as listed on the attached Form PTO 1449 are submitted herewith for placement in the file. No certification or fees are deemed necessary.

Throughout the pendency of this application, please charge any additional fees, including any required extension of time fees, and credit all overpayments to deposit account 50-1302. A duplicate of this sheet is enclosed.

Respectfully submitted,

HICKMAN PALERMO TRUONG & BECKER LLP

Dated: December 30, 2003



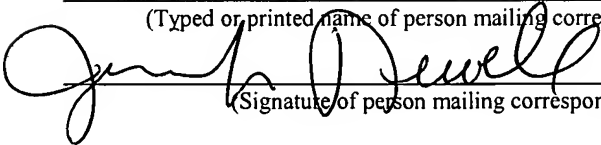
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December 31, 2003
(Date of Deposit)

Jennifer Newell

(Typed or printed name of person mailing correspondence)



(Signature of person mailing correspondence)

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)	ATTY. DOCKET NO. 15437-0579	SERIAL NO. 10/661,413
		
APPLICANT: Eric C. Saxe, et al.		
FILING DATE: September 12, 2003		GROUP: NYA

U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
		5,937,187		Kosche et al.	08-10-1999	
		5,960,461		Frank et al.	09-28-1999	
		6,002,870		Faulkner et al.	12-14-1999	
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		2003/0014612	A1	Joy et al.	01-16-2003	
		2003/0023794	A1	Venkitakrishnan et al.	01-30-2003	

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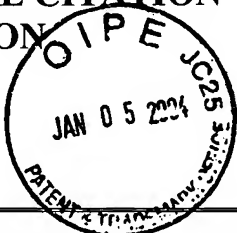
Exam. Initial*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (If known)				

Examiner Signature	Date Considered
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¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 15437-0579	SERIAL NO. 10/661,413
				APPLICANT: Eric C. Saxe, et al.	
				FILING DATE: September 12, 2003	GROUP: NYA

U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
		2003/0046495	A1	Venkitakrishnan et al.	03-06-2003	
		6,542,991	B1	Joy et al.	04-01-2003	
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		6,658,447	B2	Cota-Robles	12-02-2003	

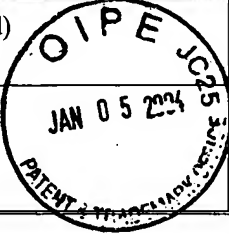
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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 15437-0579	Application Number: 10/661,413
		First Named Inventor: Eric C. Saxe	
		Filing Date: September 12, 2003	
OTHER ART – NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
		INTEL CORPORATION, "Hyper-Threading Technology on the Intel Xeon Processor Family for Servers," 2002, pp. 1-12.	
		JOEL M. TENDLER, ET AL., "IBM Server POWER4 System Microarchitecture," Technical White Paper, October 2001, pp. 1-33.	
		SANDEEP KHANNA, ET AL., "Realtime Scheduling in SunOS 5.0," undated, 16 pages.	
		J. R. EYKHOLT, ET AL., "Beyond Multiprocessing...Multithreading the SunOS Kernel," Summer '92 USENIX – June 8-June 12, 1992, San Antonio, TX, pp. 1-10.	
		R. C. AGARWAL, ET AL., "Fast Pseudorandom-Number Generators with Modulus 2 ^k or 2 ^k -1 Using Fused Multiply-Add," IBM J. RES. & DEV., VOL. 46, NO. 1, January 2002, pp. 97-116.	
		G. P. RODGERS, ET AL., "Infrastructure Requirements for a Large-Scale, Multi-Site VLSI Development Project," IBM J. RES. & DEV., VOL. 46, NO. 1, January 2002, pp. 87-95.	
		D. C. BOSSEN, ET AL., "Fault-Tolerant Design of the IBM pSeries 690 System Using POWER4 Processor Technology," IBM J. RES. & DEV., VOL. 46, NO. 1, January 2002, pp. 77-86.	
		J. M. LUDDEN, ET AL., "Functional Verification of the POWER4 Microprocessor and POWER4 Multiprocessor Systems," IBM J. RES. & DEV., VOL. 46, NO. 1, January 2002, pp. 53-76.	
		J. D. WARNOCK, ET AL., "The Circuit and Physical Design of the POWER4 Microprocessor," IBM J. RES. & DEV., VOL. 46, NO. 1, January 2002, pp. 27-51.	
		J. M. TENDLER, ET AL., "POWER4 System Microarchitecture," IBM J. RES. & DEV., VOL. 46, NO. 1, January 2002, pp. 5-25.	
		Vijay T. Lund, "Preface," IBM J. RES. & DEV., VOL. 46, NO. 1, January 2002, pp. 3-4.	

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